

WHAT IS CLAIMED IS:

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1. A liquid crystal display apparatus comprising:
 - a pair of substrates disposed opposing each other;
 - a liquid crystal layer sandwiched therebetween;
 - a data signal transmission line for supplying data signals, laid out on one of the substrates;
 - a scan signal transmission line for supplying timing signals, laid out on the one of the substrates,
 - the data signal transmission line and scan signal transmission line being arranged to intersect each other;
 - a thin-film transistor including a gate electrode which is diverted from the scan signal transmission line, electrically connected to the data and scan signal transmission lines;
 - an interlayer insulating film disposed so as to cover part of the data/scan signal transmission lines and transistor; and
 - a pixel electrode disposed on the interlayer insulating film, the pixel electrode being electrically connected to a drain electrode of the thin film transistor via a contact hole which is provided in the interlayer insulating film,
 - wherein the interlayer insulating film and liquid crystal layer are disposed so as to overlies at least part of the drain electrode.
 2. The liquid crystal display apparatus of claim 1, wherein the pixel electrode is provided with an opening formed in a

region thereof on at least part of the drain electrode.

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A23 → 3. The liquid crystal display apparatus of claim 2, wherein the opening of the pixel electrode is formed so as to contact with an outer periphery of the pixel electrode.

4. The liquid crystal display apparatus of claim 1, wherein a to-be-corrected portion is formed in at least part of the drain electrode by narrowing the width of the electrode.

5. The liquid crystal display apparatus of claim 1, wherein the interlayer insulating film is provided with an opening formed in a region thereof on at least part of the drain electrode.

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A24 → 6. A liquid crystal display apparatus comprising:
a pair of substrates disposed opposing each other;
a liquid crystal layer sandwiched therebetween;
a data signal transmission line for supplying data signals,
laid out on one of the substrates;
a scan signal transmission line for supplying timing
signals, laid out on the one of the substrates,
the data signal transmission line and scan signal
transmission line being arranged to intersect each other;
a thin-film transistor including a gate electrode which
is diverted from the scan signal transmission line,
electrically connected to the data and scan signal transmission
lines;
an interlayer insulating film disposed so as to cover part

of the data/scan signal transmission lines and transistor; and
a pixel electrode disposed on the interlayer insulating
film, the pixel electrode being electrically connected to a
drain electrode of the thin film transistor via a contact hole
which is provided in the interlayer insulating film,

wherein the interlayer insulating film and liquid crystal
layer are disposed so as to overlie at least part of the gate
electrode.

7. The liquid crystal display apparatus of claim 6, wherein
the pixel electrode is provided with an opening formed in a
region thereof on at least part of the gate electrode.

8. The liquid crystal display apparatus of claim 7, wherein
the opening of the pixel electrode is formed so as to contact
with an outer periphery of the pixel electrode.

9. The liquid crystal display apparatus of claim 6, wherein
a to-be-corrected portion is formed in at least part of the gate
electrode by narrowing the width of the electrode.

10. The liquid crystal display apparatus of claim 6, wherein
the interlayer insulating film is provided with an opening
formed in a region thereof on at least part of the gate electrode.

11. A liquid crystal display apparatus comprising:
a pair of substrates disposed opposing each other;
a liquid crystal layer sandwiched therebetween;
a data signal transmission line for supplying data signals,
laid out on one of the substrates;

a scan signal transmission line for supplying timing signals, laid out on the one of the substrates;

an auxiliary capacitance line for forming an auxiliary capacitance,

the data signal transmission line, scan signal transmission line and auxiliary capacitance line being arranged to intersect each other;

a thin-film transistor including a gate electrode which is diverted from the scan signal transmission line, electrically connected to the data and scan signal transmission lines,

an interlayer insulating film disposed so as to cover part of the data/scan signal transmission lines and transistor; and

a pixel electrode disposed on the interlayer insulating film, the pixel electrode being electrically connected to a drain electrode of the thin film transistor via a contact hole which is provided in the interlayer insulating film,

wherein the interlayer insulating film and liquid crystal layer are disposed so as to overlie part of a diverted portion which is diverted from the auxiliary capacitance line.

12. The liquid crystal display apparatus of claim 11, wherein the pixel electrode is provided with an opening formed in a region on part of the diverted portion of the auxiliary capacitance line.

13. The liquid crystal display apparatus of claim 12, wherein

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the opening of the pixel electrode is formed so as to contact with an outer periphery of the pixel electrode.

14. The liquid crystal display apparatus of claim 11, wherein the interlayer insulating film is provided with an opening formed in a region thereof on part of the diverted portion of the auxiliary capacitance line.

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15. A liquid crystal display apparatus comprising:
a pair of substrates disposed opposing each other;
a liquid crystal layer sandwiched therebetween,
a data signal transmission line for supplying data signals, laid out on one of the substrates;
a scan signal transmission line for supplying timing signals, laid out on the one of the substrates,
the data signal transmission line and scan signal transmission line being arranged to intersect each other;
a thin-film transistor including a gate electrode which is diverted from the scan signal transmission line, electrically connected to the data and scan signal transmission lines;
an interlayer insulating film disposed so as to cover part of the data/scan signal transmission lines and transistor; and
a pixel electrode disposed on the interlayer insulating film, the pixel electrode being electrically connected to a drain electrode of the thin film transistor via a contact hole which is provided in the interlayer insulating film,

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wherein the pixel electrode is provided with an opening formed at a specified location nearest to the data signal transmission line at an outer periphery of the pixel electrode.

16. The liquid crystal display apparatus of claim 15, wherein the opening of the pixel electrode is in contact with a specific location at which the pixel electrode is nearest to the outermost scan signal transmission line, and is formed in a region on at least part of the drain electrode.

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17. A liquid crystal display apparatus comprising:
a pair of substrates opposing each other;
a liquid crystal layer sandwiched therebetween;
a data signal transmission line disposed on one of the substrates;

a switching element disposed on the one substrate;
an interlayer insulating film formed so as to cover at least part of the data signal transmission line and switching element;
and

a pixel electrode disposed on the interlayer insulating film;

the switching element being interposed between the data signal transmission line and the pixel electrode,

the pixel electrode being electrically connected to the switching element via a contact hole formed in the interlayer insulating film,

wherein the pixel electrode is disposed in a residual

